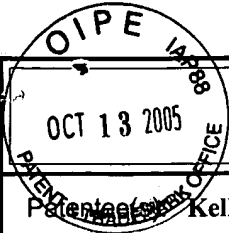


10/707250 COFC



TRANSMITTAL LETTER
(General - Patent Issued)

Docket No.
BUR920030145US1

Patent Agent: Kelkar

U.S. Patent No.
6,943,608 B2

Issue Date
September 13, 2005

Title: **WIDE FREQUENCY RANGE VOLTAGE-CONTROLLED OSCILLATORS (VCO)**

COMMISSIONER FOR PATENTS:

Certificate
OCT 19 2005
of Correction

Transmitted herewith is:

Certificate of Correction
Postcard

No fee payment is necessary. Errors were made by the U.S. Patent Office.

- ☒ No additional fee is required.
- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account **09-0456** as described below.
 - ☐ Charge the amount of _____
 - ☒ Credit any overpayment.
 - ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Jack P. Friedman
Signature

Dated: **10-10-2005**

Jack P. Friedman, Ph.D.
Reg. No. 44,688
Schmeiser, Olsen & Watts
3 Lear Jet Lane, Suite 201
Latham, NY 12110
(518) 220-1850

Customer No.: **30449**

CC:

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on	
10-10-2005	(Date)
<i>Betty Zuelsdorf</i>	
Signature of Person Mailing Correspondence	
Betty Zuelsdorf	
Typed or Printed Name of Person Mailing Correspondence	

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 6,943,608 B2

DATED : September 13, 2005

INVENTOR(S) : Kelkar

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9

Line 53, delete "or" and insert --of--

Column 10

Line 9, delete "on" and insert --on a--

Line 26, delete "of gate" and insert --of a gate--

Line 29, delete "terminal or" and insert --terminal of"

Line 35, delete "transistor gate" and insert --transistor--

Column 12

Line 19, delete "cross-output circuit" and insert --cross-output transistor circuit--

MAILING ADDRESS OF SENDER (Please do not use customer number

Jack P. Friedman, Ph.D.
Reg. No. 44,688
Schmeiser, Olsen & Watts
3 Lear Jet Lane, Suite 201
Latham, NY 12110

PATENT NO. 6,943, 608 B2

No. of additional copies

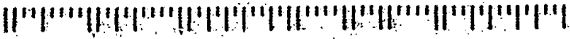


OCT 21 2005

SCHMEISER, OLSEN & WATTS
INTELLECTUAL PROPERTY LAW
3 LEAR JET LANE, SUITE 201
LATHAM, NY 12110

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

22313+1450-SO 8093



0000016592

